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- ☐ 1. **A folded-cascode switched OTA based on current deviation**
 Salinas-Cruz, R.; Espinosa-Flores-V, G.;
Devices, Circuits and Systems, 2002. Proceedings of the Fourth IEEE Internat Conference on
 17-19 April 2002 Page(s):C024 - C1-4
 Digital Object Identifier 10.1109/ICCDCS.2002.1004008
[AbstractPlus](#) | Full Text: [PDF](#)(350 KB) IEEE CNF
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- ☐ 2. **Low voltage fully differential opamp with high gain wide bandwidth suital capacitor applications**
 Hammouda, S.A.; Tawfik, M.S.; Ragaie, H.F.;
Circuits and Systems, 2002. MWSCAS-2002. The 2002 45th Midwest Sympos Volume 1, 4-7 Aug. 2002 Page(s):I - 324-7 vol.1
[AbstractPlus](#) | Full Text: [PDF](#)(312 KB) IEEE CNF
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- ☐ 3. **Switched-capacitor body-biasing technique for very low voltage CMOS ai**
 Monsurro, P.; Pennisi, S.; Scotti, G.; Trifiletti, A.;
Circuit Theory and Design, 2005. Proceedings of the 2005 European Conferen Volume 1, 28 Aug.-2 Sept. 2005 Page(s):I/257 - I/260 vol. 1
 Digital Object Identifier 10.1109/ECCTD.2005.1522959
[AbstractPlus](#) | Full Text: [PDF](#)(236 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 4. **Biasing technique via bulk terminal for minimum supply CMOS amplifiers**
 Monsurro, P.; Scotti, G.; Trifiletti, A.; Pennisi, S.;
Electronics Letters
 Volume 41, Issue 14, 7 July 2005 Page(s):779 - 780
 Digital Object Identifier 10.1049/el:20051647
[AbstractPlus](#) | Full Text: [PDF](#)(68 KB) IEE JNL
- ☐ 5. **A 1.8 V pseudo-differential switched-capacitor amplifier**
 Gunay, Z.S.; Soenen, E.G.; Embabi, S.; Sanchez-Sinencio, E.;
Custom Integrated Circuits Conference, 1998., Proceedings of the IEEE 1998 11-14 May 1998 Page(s):373 - 376
 Digital Object Identifier 10.1109/CICC.1998.695002

[AbstractPlus](#) | Full Text: [PDF\(324 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **6. 1.2-V CMOS op-amp with a dynamically biased output stage**
Giustolisi, G.; Palmisano, G.; Segreto, T.;
[Solid-State Circuits, IEEE Journal of](#)
Volume 35, Issue 4, April 2000 Page(s):632 - 636
Digital Object Identifier 10.1109/4.839923

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(228 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **7. A low voltage 10Gbps SiGe laser diode driver using adaptive RC compen common-mode feedback based current generators**
Maxim, A.;
[VLSI Circuits, 2004. Digest of Technical Papers. 2004 Symposium on](#)
17-19 June 2004 Page(s):400 - 403

[AbstractPlus](#) | Full Text: [PDF\(482 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **8. A CMOS class-AB transconductance amplifier for switched-capacitor app**
Rijns, J.J.F.; Wallinga, H.;
[Circuits and Systems, 1990., IEEE International Symposium on](#)
1-3 May 1990 Page(s):2801 - 2804 vol.4
Digital Object Identifier 10.1109/ISCAS.1990.112592

[AbstractPlus](#) | Full Text: [PDF\(272 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **9. Fully-differential CMOS current-mode amplifiers and filters**
Zelev, R.H.; Allstot, D.J.; Fiez, T.S.;
[Circuits and Systems, 1991. Conference Proceedings, China., 1991 Internation](#)
16-17 June 1991 Page(s):781 - 784 vol.2
Digital Object Identifier 10.1109/CICCAS.1991.184477

[AbstractPlus](#) | Full Text: [PDF\(272 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **10. Design of a programmable slew-rate op amp**
Shin, J.M.; Yoon, K.S.;
[Circuits and Systems, 1994., Proceedings of the 37th Midwest Symposium on](#)
Volume 1, 3-5 Aug. 1994 Page(s):142 - 146 vol.1
Digital Object Identifier 10.1109/MWSCAS.1994.519209

[AbstractPlus](#) | Full Text: [PDF\(372 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **11. Minimization of charge transfer errors in switched-capacitor stages**
Klein, H.-W.; Engl, W.L.;
[Solid-State Circuits, IEEE Journal of](#)
Volume 18, Issue 6, Dec 1983 Page(s):764 - 766

[AbstractPlus](#) | Full Text: [PDF\(272 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **12. Fully-differential CMOS current-mode circuits**
Zelev, R.H.; Allstot, D.J.; Fiez, T.S.;
[Custom Integrated Circuits Conference, 1991., Proceedings of the IEEE 1991](#)
12-15 May 1991 Page(s):24.1/1 - 24.1/4
Digital Object Identifier 10.1109/CICC.1991.164055

[AbstractPlus](#) | Full Text: [PDF\(296 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **13. Determination of stability using return ratios in balanced fully differential circuits**
Hurst, P.J.; Lewis, S.H.;
[Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on](#)
[Circuits and Systems II: Express Briefs, IEEE Transactions on](#)
Volume 42, Issue 12, Dec. 1995 Page(s):805 - 817
Digital Object Identifier 10.1109/82.476178
[AbstractPlus](#) | [Full Text: PDF\(1228 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ **14. Active current mirror receiver**
Gauthier, C.R.; Brown, R.B.;
[Circuits and Systems, 2000. Proceedings of the 43rd IEEE Midwest Symposium](#)
Volume 2, 8-11 Aug. 2000 Page(s):706 - 709 vol.2
Digital Object Identifier 10.1109/MWSCAS.2000.952854
[AbstractPlus](#) | [Full Text: PDF\(392 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **15. An high-swing, 1.8 V, push-pull opamp for sigma-delta modulators**
Malcovati, P.; Maloberti, F.; Terzani, M.;
[Electronics, Circuits and Systems, 1998 IEEE International Conference on](#)
Volume 1, 7-10 Sept. 1998 Page(s):33 - 36 vol.1
Digital Object Identifier 10.1109/ICECS.1998.813265
[AbstractPlus](#) | [Full Text: PDF\(292 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **16. Fully-differential CMOS current-mode circuits and applications**
Zelev, R.H.; Allstot, D.J.; Fiez, T.S.;
[Circuits and Systems, 1991., IEEE International Symposium on](#)
11-14 June 1991 Page(s):1817 - 1820 vol.3
Digital Object Identifier 10.1109/ISCAS.1991.176758
[AbstractPlus](#) | [Full Text: PDF\(480 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **17. A high-performance current mirror using SOI gate controlled hybrid transistors**
Huang, R.; Zhang, X.; Wang, Y.Y.;
[SOI Conference, 1998. Proceedings., 1998 IEEE International](#)
5-8 Oct. 1998 Page(s):97 - 98
Digital Object Identifier 10.1109/SOI.1998.723129
[AbstractPlus](#) | [Full Text: PDF\(192 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **18. A low-noise low-offset capacitive sensing amplifier for a 50-/spl mu/g/sp monolithic CMOS MEMS accelerometer**
Jiangfeng Wu; Fedder, G.K.; Carley, L.R.;
[Solid-State Circuits, IEEE Journal of](#)
Volume 39, Issue 5, May 2004 Page(s):722 - 730
Digital Object Identifier 10.1109/JSSC.2004.826329
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(696 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ **19. Gain and bandwidth boosting techniques for high-speed operational amplifiers**
Amourah, M.M.; Geiger, R.L.;
[Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Symposium on](#)
Volume 1, 6-9 May 2001 Page(s):232 - 235 vol. 1
Digital Object Identifier 10.1109/ISCAS.2001.921833
[AbstractPlus](#) | [Full Text: PDF\(312 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **20. New low-voltage fully-balanced wide-band differential CMOS current amp**
Bendong Sun; Fei Yuan;
Circuits and Systems, 2002. MWSCAS-2002. The 2002 45th Midwest Sympos
Volume 2, 4-7 Aug. 2002 Page(s):II-57 - II-60 vol.2
[AbstractPlus](#) | [Full Text: PDF\(302 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **21. Simple rail-to-rail constant-transconductance input stage operating in st**
Prodanov, V.I.; Green, M.M.;
Circuits and Systems, 1996., IEEE 39th Midwest symposium on
Volume 2, 18-21 Aug. 1996 Page(s):957 - 960 vol.2
Digital Object Identifier 10.1109/MWSCAS.1996.588120
[AbstractPlus](#) | [Full Text: PDF\(320 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **22. Integration of cryocooled superconducting analog-to-digital converter an**
amplifier
Gupta, D.; Kadin, A.M.; Webber, R.J.; Rochwarger, I.; Bryce, D.; Hollander, W
Yim; Channakeshav; Kraft, R.P.; Jin-Woo Kim; McDonald, J.F.;
Applied Superconductivity, IEEE Transactions on
Volume 13, Issue 2, Part 1, June 2003 Page(s):477 - 483
Digital Object Identifier 10.1109/TASC.2003.813911
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(1260 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ **23. A fast settling, high DC gain, low power OPAMP design for high resolutio**
A/D converters
Shahrjerdi, D.; Hekmatshoar, B.; Talaie, M.; Shoaie, O.;
Microelectronics, 2003. ICM 2003. Proceedings of the 15th International Confe
9-11 Dec. 2003 Page(s):207 - 210
Digital Object Identifier 10.1109/ICM.2003.1287771
[AbstractPlus](#) | [Full Text: PDF\(1455 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **24. Low-power low-voltage differential class-AB OTAs for SC circuits**
Carvajal, R.G.; Galan, J.; Ramirez-Angulo, J.; Torralba, A.;
Electronics Letters
Volume 38, Issue 22, 24 Oct 2002 Page(s):1304 - 1305
[AbstractPlus](#) | [Full Text: PDF\(342 KB\)](#) IEEE JNL

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L2	109	1 and emitter adj degeneration	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/23 10:41
L3	21	2 and (inverse or inversely)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/23 10:41